

The diagram illustrates the electrical connections between a Drive IC (6) and a video display panel (1). The Drive IC contains a Signal Generation Circuit (6a) and a Power Supply Circuit (6b). The video display panel includes a V Driver (5), a Drain Line (2), a Gate Line (3), an HSW (4), and an H Driver (3). The connections are as follows:

- Power and Control Lines (Top):** VCLK, VVDD, VST, ENB, and VVSS connect to the Drive IC and the V Driver.
- Signal Lines (Bottom):** Video, HST, HCLK, HVDD, and HVSS connect to the Drive IC and the H Driver.
- Internal Panel Connections:** The V Driver is connected to the Drain Line. The Gate Line is connected to the HSW and the H Driver.
- Pixel Circuitry:** The Drain Line and Gate Line intersect at a pixel circuit (2), which includes a transistor (2a), a switch (2b), and a storage capacitor (2c, 2d, 2e).

FIG.2

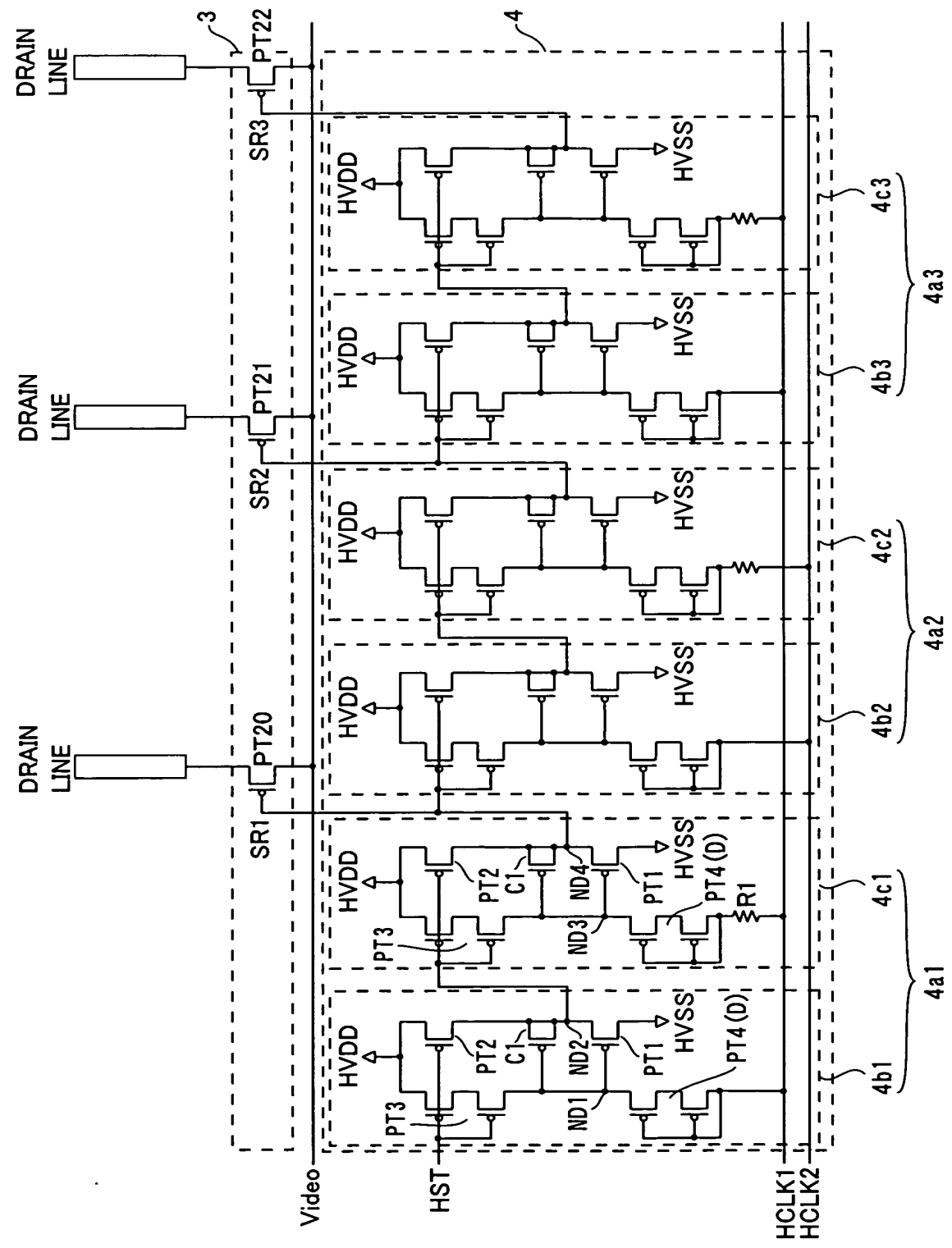


FIG.3

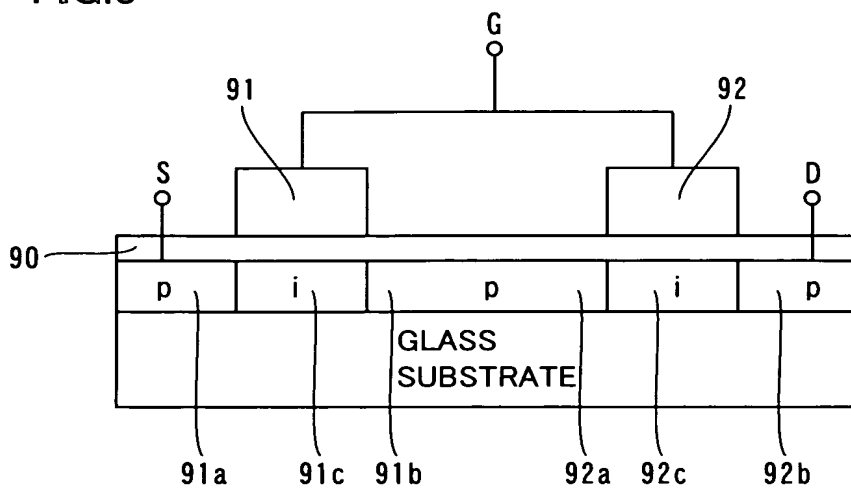


FIG.4

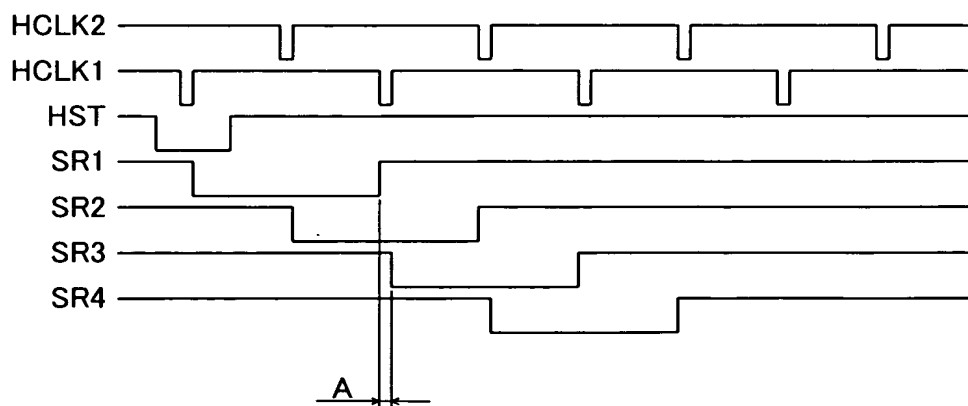


FIG.5

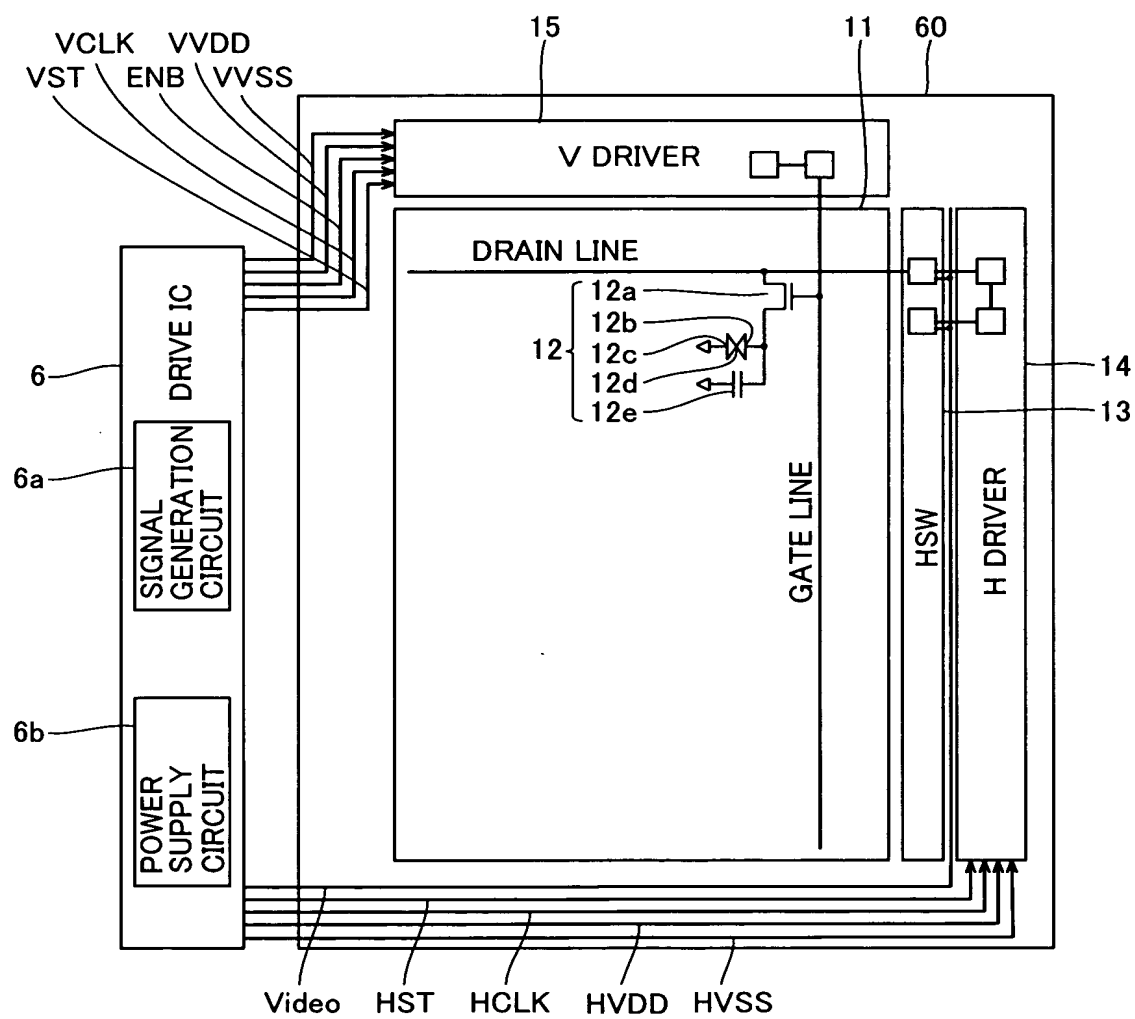


FIG. 6

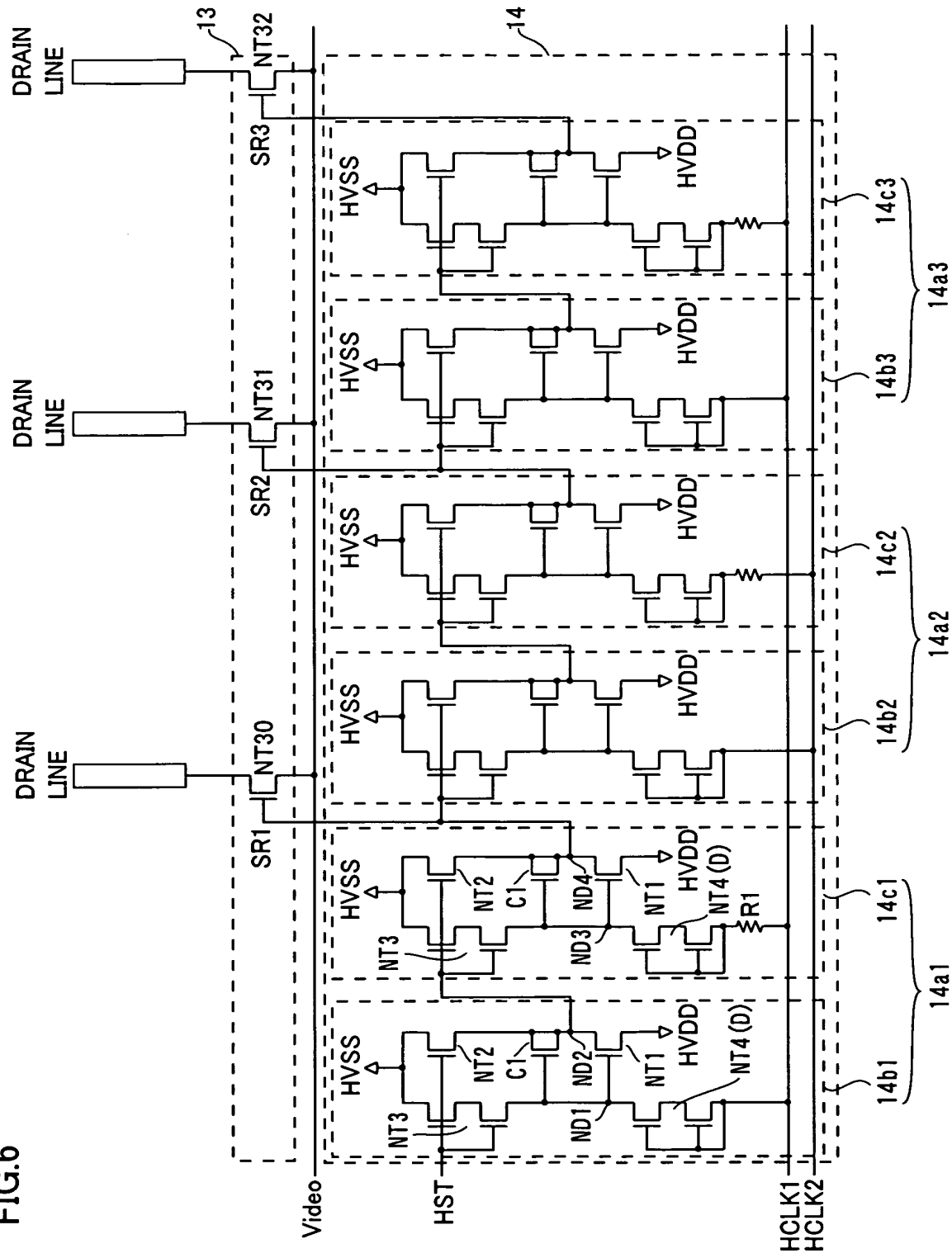


FIG.7

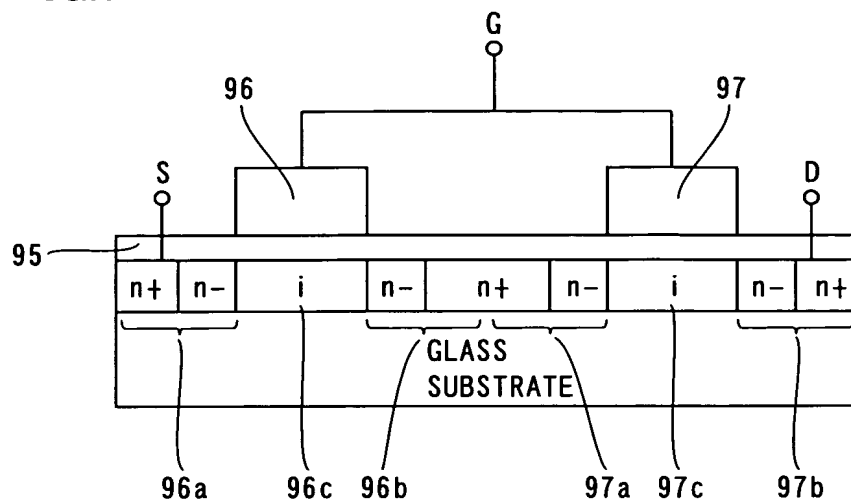


FIG.8

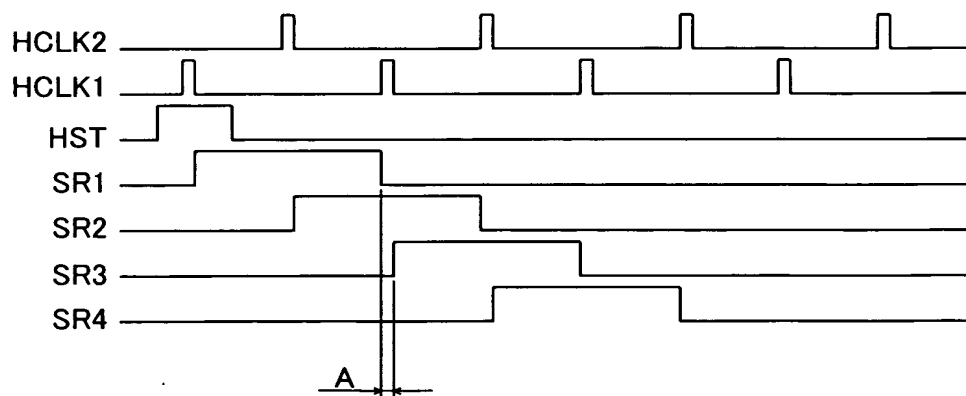


FIG.10

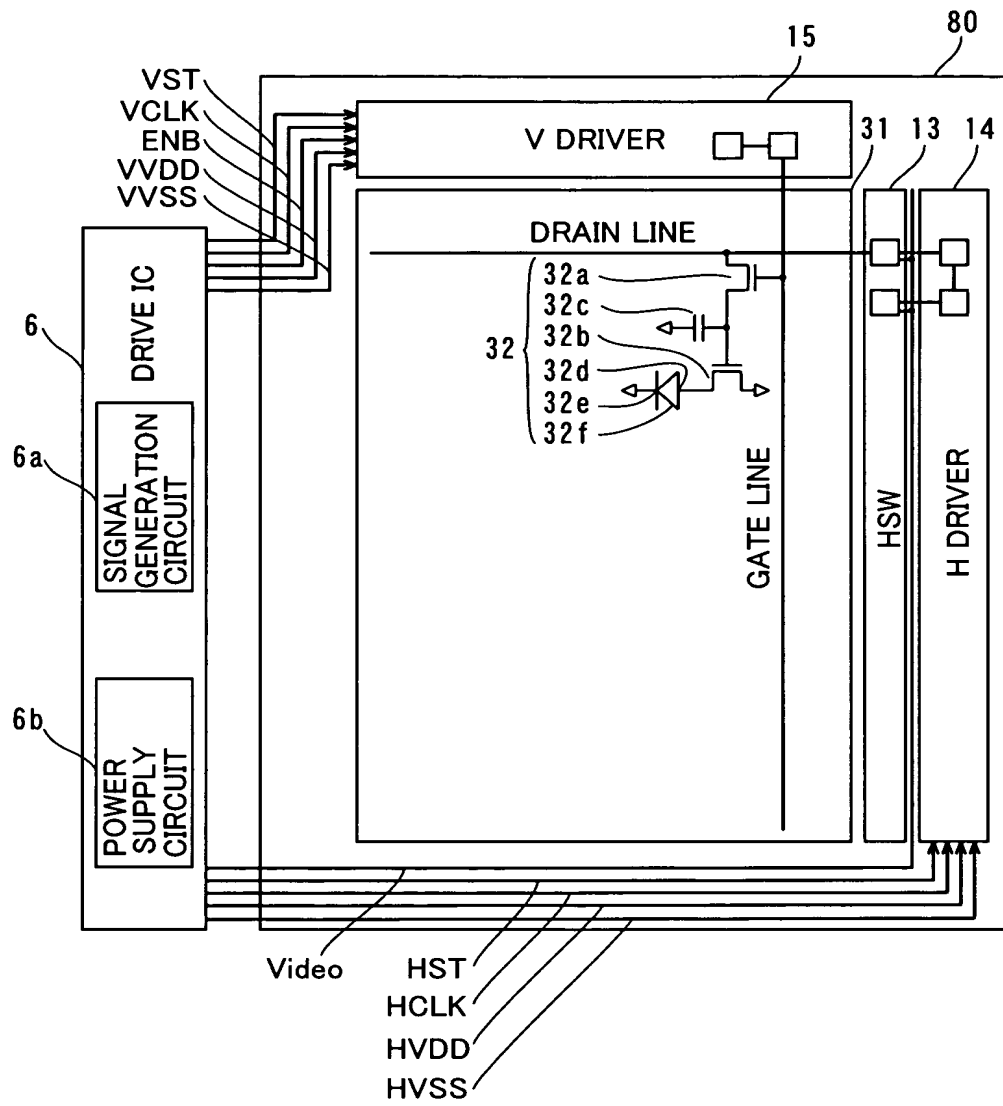


FIG.11

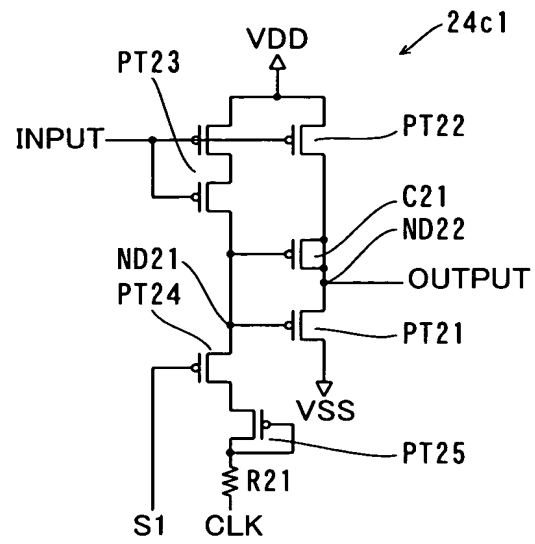


FIG.12

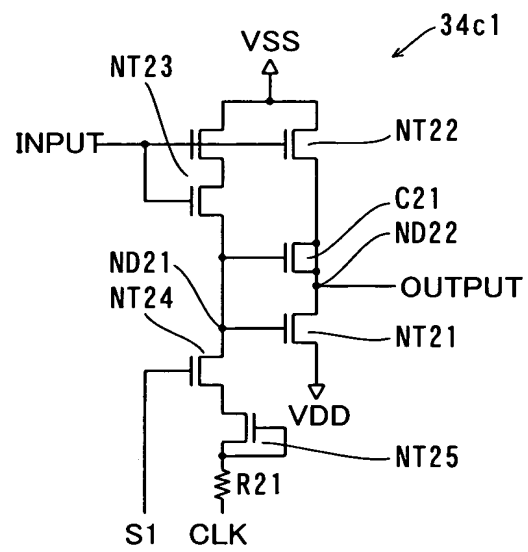


FIG.13 PRIOR ART

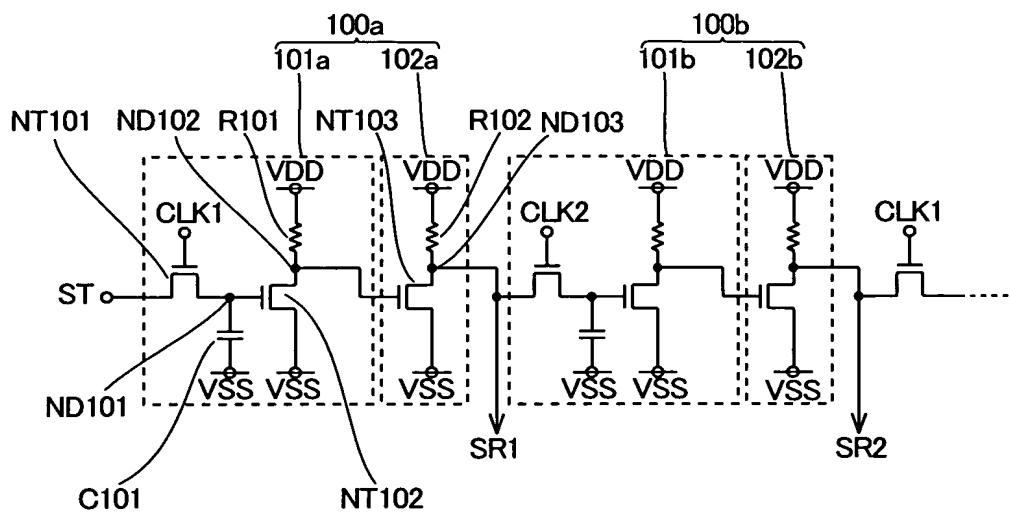


FIG.14 PRIOR ART

